

Embedding Chaos

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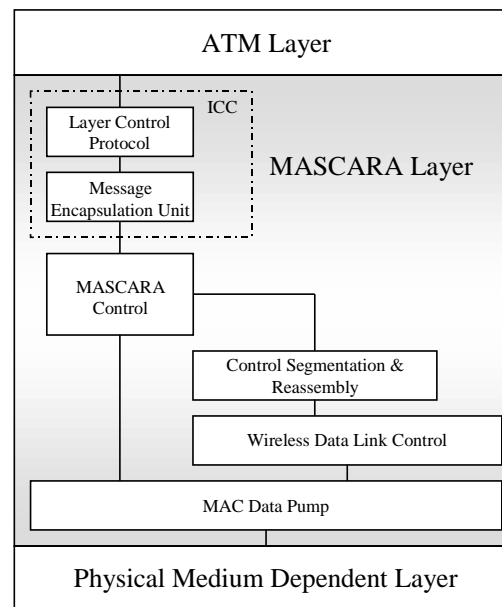
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Motivation & starting point

- Verification/model checking of Mascara
 - wireless industrial ATM medium-access protocol for LANs
 - developed within [Wand](#) industrial board
 - given in [SDL](#)



Model checking

+ “automatic” (“push-button”) program verification method

$$p \models \varphi$$

– state-space explosion

– How to obtain the model from a piece of software?

SDL

- “Specification Description Language” [SDL92, 1992]
- standardized (in various versions)
- standard spec. language for telecom applications
- language **characteristics**:
 - * **control** structure: **communicating finite-state machines**
 - * **communication**: **asynchronous** message passing
 - * **data**: various basic and composed types
 - * **timers** and **time-outs**¹
 - * bells and whistles: graphical notation, structuring mechanisms, OO, ...

¹no real-time, not uncontroversial

Model checking SDL

- Various **aggravations**
 1. it's about **software** (data)
 2. it's about **large**² software
 3. it's about **open** systems
- **approaches**:
 1. **abstraction**:
 - (a) **data** abstraction: replace concrete domains by **finite**, **abstract** ones
 - (b) **control** abstraction, i.e., add **non-determinism**
 2. **decompose** system along SDL-blocks

²well, depends

Model checking SDL in theory (and in practice)

- in theory

1. cut out a sub-component
2. model its environment abstractly, i.e.,
⇒ add an environment *process* which
 - closes the sub-component
 - shows more behavior than the real environment ⇒ *in extremis*: add chaos-process
3. push the button ...

- in practice

- components and interfaces might be large
- closing is tedious
- SDL-tools (or others) don't often work with abstract data

Model checking open SDL systems

- three more specific **problems**
 1. **asynchronous** input queues
 2. (infinite data domains)
 3. chaotic **timer** behavior
- three specific *practical* solutions
 1. no **external** chaos process

“embedding chaos”
 2. **one-valued** data abstraction (= no external data)
 3. three-valued **timer abstraction**

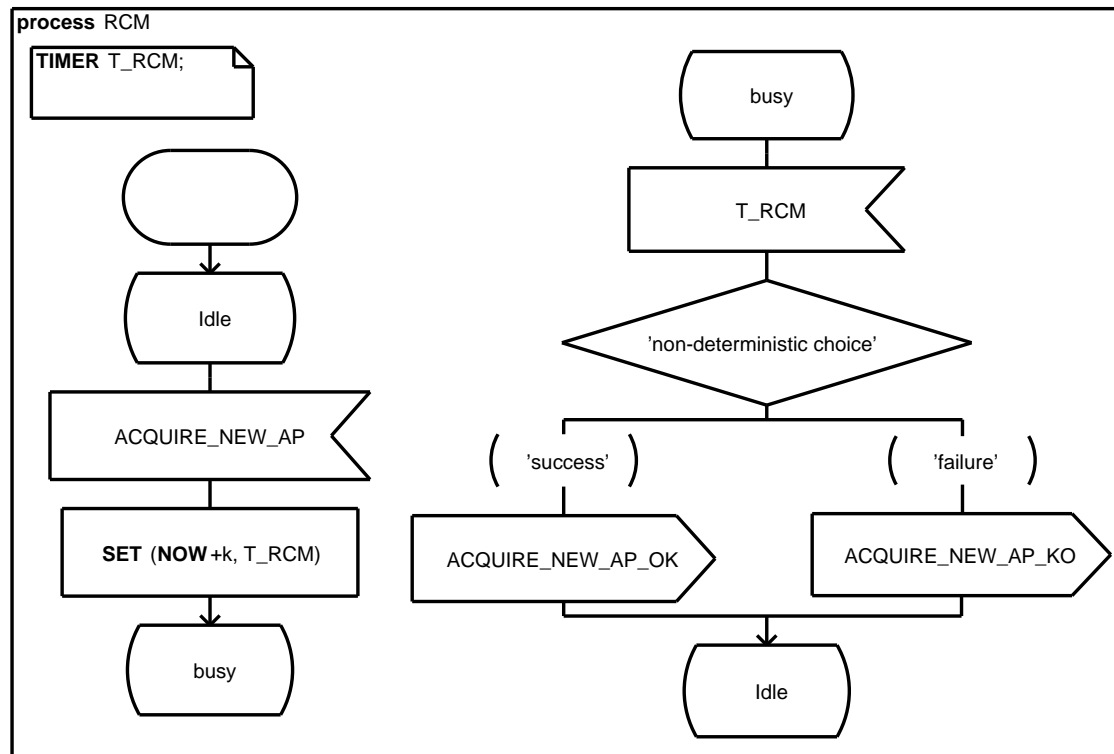
Goal

- Transformation
- automatic
- yielding a closed system
- safe abstraction
- executable with standard SDL-semantics \Rightarrow source code transformation.

Roadmap

1. (sketch) of syntax
2. SO-**semantics** of SDL
 - (a) local rules
 - (b) parallel composition
3. semantics of **timers**
4. closing the system via data-flow analysis
5. dealing with chaotic timers

Syntax: Example



Syntax

- guarded, labelled **edges** $l \longrightarrow_{\alpha} \hat{l}$ connecting **locations**
- **actions** α : (with **guards** g)
 - **input**: $?s(x)$
 - **output**: $g \triangleright P!s(e)$
 - **assignment**: $g \triangleright x := e$

Semantics (local)

- straightforward **operational** small-step semantics
 - **interleaving** semantics
 - **top-level** concurrency
 - **local** (= 1 process) **configuration**:
 1. **location**/control state
 2. **valuation** of variables
 3. content of **input-queue**
- ⇒ **labelled** steps between configurations

Semantics cont'd (local rules)

$$\begin{array}{c}
 \frac{l \longrightarrow_{?s(x)} \hat{l} \in Edg}{(l, \eta, (s, v) :: q) \rightarrow_{\tau} (\hat{l}, \eta[x \mapsto v], q)} \text{ INPUT} \\
 \\
 \frac{l \longrightarrow_{?s'(x)} \hat{l} \in Edg \Rightarrow s' \neq s}{(l, \eta, (s, -) :: q) \rightarrow_{\tau} (l, \eta, q)} \text{ DISCARD} \\
 \\
 \frac{l \longrightarrow_{g \triangleright P!(s,e)} \hat{l} \in Edg \quad \llbracket g \rrbracket_{\eta} = true \quad \llbracket e \rrbracket_{\eta} = v}{(l, \eta, q) \rightarrow_{P!(s,v)} (\hat{l}, \eta, q)} \text{ OUTPUT} \\
 \\
 \frac{(l, \eta, q) \rightarrow_{P!(s,v)} (\hat{l}, \eta, q)}{v \in D} \text{ RECEIVE} \\
 \\
 \frac{(l, \eta, q) \rightarrow_{P?(s,v)} (l, \eta, q :: (s, v)) \quad l \longrightarrow_{g \triangleright x:=e} \hat{l} \in Edg \quad \llbracket g \rrbracket_{\eta} = true \quad \llbracket e \rrbracket_{\eta} = v}{(l, \eta, q) \rightarrow_{\tau} (\hat{l}, \eta[x \mapsto v], q)} \text{ ASSIGN}
 \end{array}$$

Timers in SDL

- no **real-time**
 - **discrete-time** semantics, as in the *DTSpin* (“discrete time *Spin*”) model-checker [Bošnački and Dams, 1998, DTSpin2000, 2000]
- ⇒ time evolves by **ticking down (active) timer variables**
- timer: active or deactivated
 - **timeout** possible: if active timer has reached 0
 - modelled by **time-out guards** (cf. [Bošnački et al., 2000])

Syntax for timers

- 3 (guarded) actions involving timers

set $g \triangleright \text{set } t := e$ (re-)activate timer for period given by e .

reset $g \triangleright \text{reset } t$: deactivate

timeout $g_t \triangleright \text{reset } t$ performing a timeout, thereby deactivate t

- Note: timeout is guarded by “timer-guard” g_t

Semantics cont'd (local timer rules)

$$\begin{array}{c}
 \frac{l \longrightarrow_{g \triangleright \text{set } t := e} \hat{l} \in Edg \quad \llbracket g \rrbracket_{\eta} = true \quad \llbracket e \rrbracket_{\eta} = v}{(l, \eta, q) \rightarrow_{\tau} (\hat{l}, \eta[t \mapsto on(v)], q)} \text{ SET} \\
 \\
 \frac{l \longrightarrow_{g \triangleright \text{reset } t} \hat{l} \in Edg \quad \llbracket g \rrbracket_{\eta} = true}{(l, \eta, q) \rightarrow_{\tau} (\hat{l}, \eta[t \mapsto off], q)} \text{ RESET} \\
 \\
 \frac{l \longrightarrow_{g_t \triangleright \text{reset } t} \hat{l} \in Edg \quad \llbracket t \rrbracket_{\eta} = on(0)}{(l, \eta, q) \rightarrow_{\tau} (\hat{l}, \eta[t \mapsto off], q)} \text{ TIMEOUT} \\
 \\
 \frac{(l \longrightarrow_{\alpha} \hat{l} \in Edg \Rightarrow \alpha \neq g_t \triangleright \text{reset } t) \quad \llbracket t \rrbracket_{\eta} = on(0)}{(l, \eta, q) \rightarrow_{\tau} (l, \eta[t \mapsto off], q)} \text{ TDISCARD}
 \end{array}$$

Parallel composition

- standard **product** construction
- **message passing** using the **labelled** steps
- Note: **Tick** step = counting down active timers:
 - can be taken only when **no other** move possible
 - ⇒ **tick** step has **least priority!**

$$\frac{\textit{blocked}(l, \eta, q)}{(l, \eta, q) \xrightarrow{\textit{tick}} (l, \eta[t \mapsto (t-1)], q)} \text{TICK}$$

Parallel composition (rules)

$$\frac{(\sigma_1, q_1) \rightarrow_{P!(s,v)} (\hat{\sigma}_1, \hat{q}_1) \quad (\sigma_2, q_2) \rightarrow_{P?(s,v)} (\hat{\sigma}_2, \hat{q}_2) \quad s \notin \text{Sig}_{ext}}{\text{COMM}}$$

$$\frac{(\sigma_1, q_1) \times (\sigma_2, q_2) \rightarrow_{\tau} (\hat{\sigma}_1, \hat{q}_1) \times (\hat{\sigma}_2, \hat{q}_2) \quad (\sigma_1, q_1) \rightarrow_{\lambda} (\hat{\sigma}_1, \hat{q}_1) \quad \lambda = \{\tau, P?(s,v), P!(s,v) \mid s \in \text{Sig}_{ext}\}}{\text{INTERLEAVE}}$$

$$\frac{(\sigma_1, q_1) \times (\sigma_2, q_2) \rightarrow_{\lambda} (\hat{\sigma}_1, \hat{q}_1) \times (\sigma_2, q_2) \quad \text{blocked}(l, \eta, q)}{\text{TICK}}$$

$$(l, \eta, q) \rightarrow_{tick} (l, \eta[t \mapsto (t-1)], q)$$

What's next

- **status:** semantics for **open** systems = **chaotic signal-exchange** with environment
- **goal:**
 - **no external** communication
 - **abstract data** from outside: **chaotic data value** \top
- **side-condition**
 - use **official/implemented** SDL-semantics (tools!):
 - * there are no **abstracted data** in SDL
 - * we cannot **re-implement** tick
 - keep it **simple**

The need for data-flow analysis

- **abstractly**: replace external $?s(x)$ by receiving \top

- **better**:

remove external reception actions (= replace it by τ -actions³)

- But: transformation may lead to **less** behavior

⇒ **Unsound!**

⇒ remove all variables (**potentially**) influenced by x , as well (and **transitively** so)

- $\hat{=}$ **forward slice/cone of influence**

³In SDL: NONE-transitions

Closing the program

- two steps:
 1. **Data-flow** analysis: mark all variable instances potentially influenced by chaos
 2. **transform** the program, using that marking

Data-flow analysis

- **forward** analysis
- **control-flow** (almost) directly given by SDL-automata
- modelling the **abstract effect/transfer functions** per action = **node**, e.g.:

$$\begin{aligned}
 f(?s(x))\eta^\alpha &= \begin{cases} \eta^\alpha[x \mapsto \top] \\ \eta^\alpha[x \mapsto \bigvee \{ \llbracket e \rrbracket_{\eta^\alpha} \mid \alpha_{n'} = g \triangleright P!s(e) \text{ for some node } n' \}] \end{cases} & s \in \text{Sig}_{ext} \\
 f(g \triangleright P!s(e))\eta^\alpha &= \eta^\alpha & \text{else} \\
 f(g \triangleright x := e)\eta^\alpha &= \eta^\alpha[x \mapsto \llbracket e \rrbracket_{\eta^\alpha}]
 \end{aligned}$$

- **constraint solving**: minimal solution for

$$\eta_{post}^\alpha(n) \geq f_n(\eta_{pre}^\alpha(n)) \quad (1.1)$$

$$\eta_{pre}^\alpha(n) \geq \bigvee \{ \eta_{post}^\alpha(n') \mid (n', n) \text{ in flow relation} \} \quad (1.2)$$

Worklist algo (pseudo-code)

input : the flow–graph of the program

output: $\eta_{pre}^\alpha, \eta_{post}^\alpha$;

$\eta^\alpha(n) = \eta_{init}^\alpha(n)$;

$WL = \{n \mid \alpha_n = ?s(x), s \in Sig_{ext}\}$;

repeat

 pick $n \in WL$;

 let $S = \{n' \in succ(n) \mid f_n(\eta^\alpha(n)) \not\leq \eta^\alpha(n')\}$

 in

 for all $n' \in S$: $\eta^\alpha(n') := f(\eta^\alpha(n))$;

$WL := WL \setminus n \cup S$;

until $WL = \emptyset$;

$\eta_{pre}^\alpha(n) = \eta^\alpha(n)$;

$\eta_{post}^\alpha(n) = f_n(\eta^\alpha(n))$

What about time?

- chaotic environment also means: **chaotic timed behaviour**
- so far: we ignored **timers**
- Remember: **time steps** (ticks) have **least priority!**

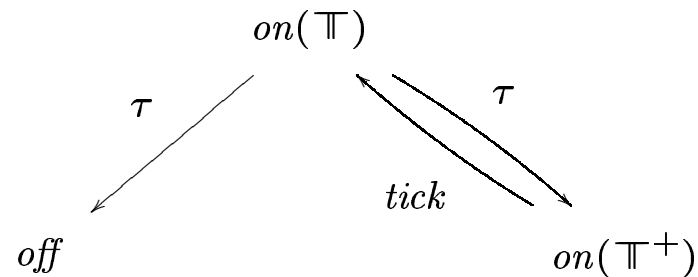
⇒ new τ steps make **ticks impossible!**

⇒ chaos = at arbitrary points

1. *sending* any possible value, **+**
2. **refusing to send** something (lest to get **less** ticks and thus less **timeouts**)

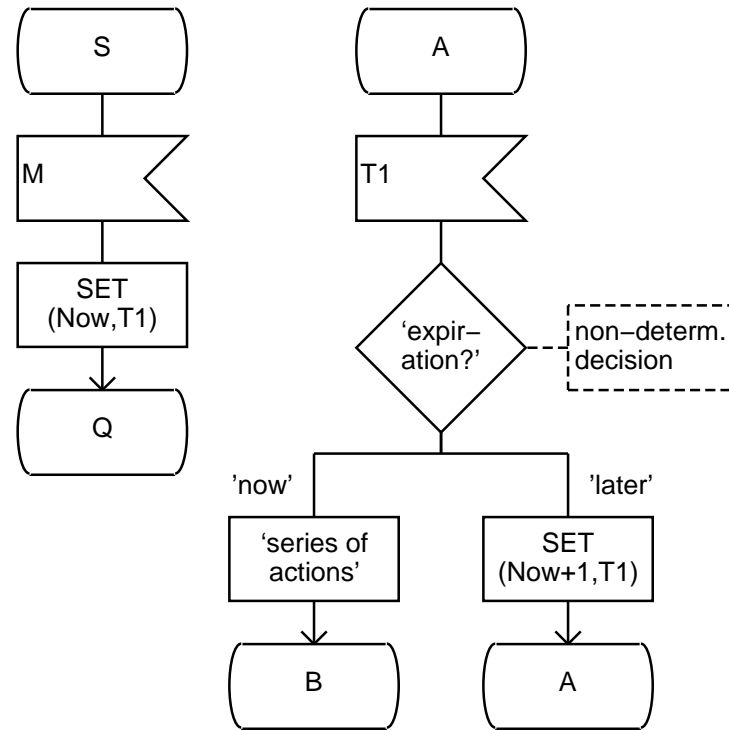
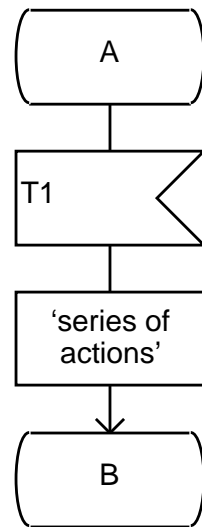
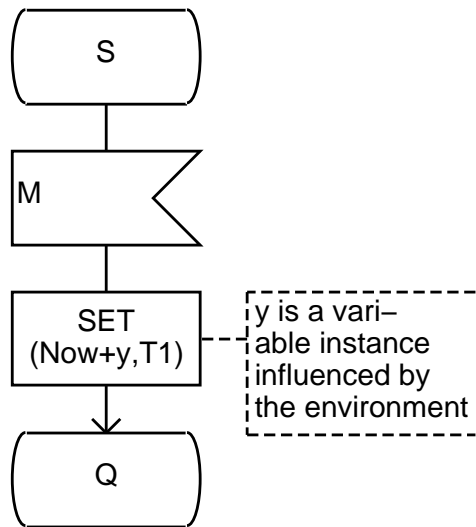
Timer abstraction

- Three abstract values:
 - de-activated
 - arbitrarily active
 - active, but not 0 (no time-out possible)
- arbitrary expiration time \Rightarrow non-deterministic setting from $on(\top)$ to $on(\top^+)$.



- **embedding** the timer: one **additional** timer t_P per process

Transformation of timers (in SDL)



Transformation rules

- e.g.: **input**⁴.

$$\frac{l \longrightarrow_{?s(x)} \hat{l} \in Edg^{\top} \quad s \in Sig_{ext}}{l \longrightarrow_{g^{\#} \triangleright skip} \hat{l} \in Edg^{\#}} \text{T-INPUT}_2$$

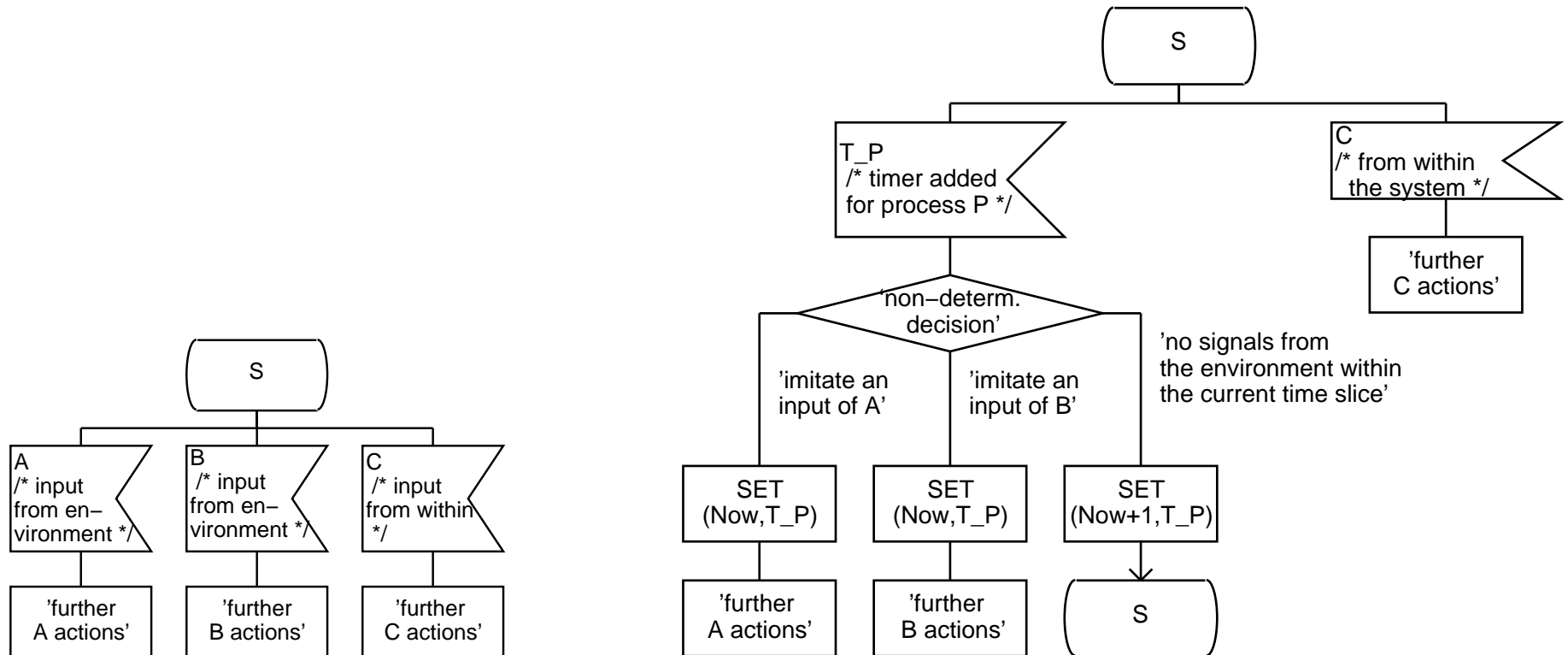
$$\frac{}{l \longrightarrow_{gt_P \triangleright set t_P:=1} l \in Edg^{\#}} \text{T-NOINPUT}$$

- e.g.: **assignement**

$$\frac{l \longrightarrow_{g \triangleright x:=e} \hat{l} \in Edg^{\top} \quad \llbracket e \rrbracket_{\eta_t^\alpha} = \top \quad g^{\#} = \llbracket g \rrbracket_{\eta_t^\alpha}}{l \longrightarrow_{g^{\#} \triangleright skip} \hat{l} \in Edg^{\#}} \text{T-ASSIGN}_2$$

⁴T-INPUT₁ and T-ASSIGN₁ do “nothing”

Transformation of inputs (in SDL)



Transformation

$$\frac{l \longrightarrow_{g \triangleright x:=e} \hat{l} \in \text{Edg}^\top \quad \llbracket e \rrbracket_{\eta_l^\alpha} \neq \top \quad g^\# = \llbracket g \rrbracket_{\eta_l^\alpha}}{\text{T-ASSIGN}_1}$$

$$\frac{l \longrightarrow_{g \triangleright x:=e} \hat{l} \in \text{Edg}^\top \quad \llbracket e \rrbracket_{\eta_l^\alpha} = \top \quad g^\# = \llbracket g \rrbracket_{\eta_l^\alpha}}{\text{T-ASSIGN}_2}$$

$$\frac{l \longrightarrow_{g^\# \triangleright skip} \hat{l} \in \text{Edg}^\# \quad s \notin \text{Sig}_{ext}}{l \longrightarrow_{?s(x)} \hat{l} \in \text{Edg}^\top} \text{T-INPUT}_1$$

$$\frac{l \longrightarrow_{?s(x)} \hat{l} \in \text{Edg}^\# \quad s \in \text{Sig}_{ext}}{l \longrightarrow_{?s(x)} \hat{l} \in \text{Edg}^\top} \text{T-INPUT}_2$$

$$\frac{l \longrightarrow_{gt_P \triangleright reset t_P} \longrightarrow_{set t_P:=0} \hat{l} \in \text{Edg}^\#}{\text{T-NOINPUT}}$$

$$\frac{l \longrightarrow_{gt_P \triangleright reset t_P} \longrightarrow_{set t_P:=1} l \in \text{Edg}^\# \quad l \longrightarrow_{g \triangleright P!(s,e)} \hat{l} \in \text{Edg}^\top \quad s \notin \text{Sig}_{ext} \quad g^\# = \llbracket g \rrbracket_{\eta_l^\alpha}}{\text{T-OUTPUT}_1}$$

$$l \longrightarrow_{g^\# \triangleright P!(s,e)} \hat{l} \in \text{Edg}^\#$$

$l \longrightarrow_{g \triangleright P!(s,e)} \hat{l} \in Edg^\top \quad s \in Sig_{ext} \quad g^\sharp = \llbracket g \rrbracket_{\eta_l^\alpha}$	T-OUTPUT ₂
$l \longrightarrow_{g \triangleright set\ t:=e} \hat{l} \in Edg^\top \quad g^\sharp = \llbracket g \rrbracket_{\eta_l^\alpha} \quad \llbracket e \rrbracket_{\eta_l^\alpha} \neq \top$	T-SET ₁
$l \longrightarrow_{g \triangleright set\ t:=e} \hat{l} \in Edg^\top \quad g^\sharp = \llbracket g \rrbracket_{\eta_l^\alpha} \quad \llbracket e \rrbracket_{\eta_l^\alpha} = \top$	T-SET ₂
$l \longrightarrow_{g \triangleright reset\ t} \hat{l} \in Edg^\top \quad g^\sharp = \llbracket g \rrbracket_{\eta_l^\alpha}$	T-RESET
$l \longrightarrow_{g_t \triangleright reset\ t} \hat{l} \in Edg^\top \quad g_t^\sharp = \llbracket g_t \rrbracket_{\eta_l^\alpha}$	T-TIMEOUT
$l \longrightarrow_{g_t \triangleright reset\ t} \hat{l} \in Edg^\sharp$ $\llbracket t \rrbracket_{\eta_l^\alpha} = \top$	T-NOTIMEOUT
$l \longrightarrow_{g_t \triangleright reset\ t} \longrightarrow_{set\ t:=1} l \in Edg^\sharp$	

Soundness result

Theorem. *The transformed system is **closed** and a **safe abstraction** of the original one*

- **safe abstraction**, i.e.,

$$\text{if } S^\# \models \varphi \text{ then } S \models \varphi$$

where φ is an LTL-formula⁵

Proof:

- transformed system and original in **simulation** relation

$\Rightarrow S^\#$ shows more behavior than S , i.e., it has **more traces**.

⁵which does not mention chaotically influenced variables.

Related work

- software **testing**
- e.g. [Colby et al., 1998] VERISOFT, C, untimed
- [Dwyer and Pasareanu, 1998]: **filtering** = “**refined**” chaos, but **external**
- **Module checking**:
 - checking **open systems**
 - e.g. MOCHA [Alur et al., 1998]

Future work

- implementation
- “refined” chaos
 - specified properties by LTL
 - arbitrarily chaotic timer **exporation** \Rightarrow **calulated** by data-flow analysis

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