

Abstract Interface Behavior of Object-Oriented Languages with Monitors

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Structure

introduction

semantics

interface description

- lock ownership

- data dependencies

- control dependencies

conclusion

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Introduction

- considered so far
 - **classes** and instantiation
- ⇒ heap
 - multithreading (vs. sequential/deterministic programs)
 - connectivity
- here: **synchronization**/monitors

Monitors

- shared (instance) state + concurrency \Rightarrow **mutex**
- **sync.** mechanism: **monitors**
- for instance in *Java*
- here
 - no synchronized **blocks**
 - no **wait/signal**¹
 - no **connectivity**
- but:
 - **re-entrant** monitors (recursion)
- deliverable for task 1 (“compositionality and modularity: a semantic approach”), subtask 1.c (“basic features: libraries and synchronization protocols”), cf. [2, Sec. 7.2].

¹In *Java*: wait and notify.

Why is this interesting?

- fundamental question:

what is observable of an oo program?

- Now:

*Does the addition of **monitors** **increase** or **decrease** the discriminating power or not?*

- intuitively: 2 **plausible** answers:

- the observer sees **less**!
- the observer sees **more**!

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Road map

- incorporate *monitors* into the semantics
- characterization of the interface behavior
 - *may* and *must* approximation of *lock-ownership*
- design goals
 - (preferably) *seamless* extension of the *calculus* with an eye to *compositionality*
 - ⇒ clean separation of concerns between
assumptions vs. *commitments*
- intuitively:
 - enabledness of *input* must depend *only* on the *environment* (= assumption)
 - enabledness of *output* must depend *only* on the *component* (= commitments)
- interface *trace* must contain *all* relevant information relevant (and *not* part of the internal state(s))
- cf. game theory

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Syntax

- modest changes
- objects with **locks**
- extend object = class + fields (written $o[c, F]$ to “class + fields + **lock**”

$o[c, F, n]$

(lock n = reference to thread)

Syntax

C	$::=$	$\mathbf{0} \mid C \parallel C \mid \nu(n:T).C \mid n[\![O]\!] \mid n[n, F, n] \mid n\langle t \rangle$	program
O	$::=$	F, M	object
M	$::=$	$l^u = m, \dots, l^u = m, l^s = m, \dots, l^s = m$	method suite
F	$::=$	$l^u = f, \dots, l^u = f$	fields
m	$::=$	$\varsigma(n:T).\lambda(x:T, \dots, x:T).t$	method
f	$::=$	$\varsigma(n:T).\lambda().v \mid \varsigma(n:T).\lambda().\perp_n$	field
t	$::=$	$v \mid \text{stop} \mid \text{let } x:T = e \text{ in } t$	thread
e	$::=$	$t \mid \text{if } v = v \text{ then } e \text{ else } e \mid \text{if } \text{undef}(v.l) \text{ then } e \text{ else } e \text{ expr.}$ $\mid v.l(v, \dots, v) \mid v.l := v \mid \text{currentthread}$ $\mid \text{new } n \mid \text{new}\langle t \rangle$	
v	$::=$	$x \mid n$	values

Semantics

1. operational semantics
2. remember the design-goals
3. two stages
 - internal semantics
 - closed system
 - spec. of the “virtual machine”
 - external semantics
 - interaction with environment via
 - message passing (calls/returns)

first attempt

- example: incoming call of unsynchronized method

$$\dot{\Xi} = \Xi + a \quad \dot{\Xi} \vdash [a] : T$$

$$a = \nu(\Xi'). n\langle \text{call } o_r.l(\vec{v}) \rangle? \quad t_{\text{blocked}} = \text{let } x':T' = \text{block in } t$$

$$\Xi \vdash C \parallel n\langle t_{\text{blocked}} \rangle \xrightarrow{a}$$

$$\dot{\Xi} \vdash C \parallel C(\Theta') \parallel n\langle \text{let } x:T = o_r.l(\vec{v}) \text{ in return } x; t_{\text{blocked}} \rangle$$

first attempt

- example: incoming call of synchronized method
- assume: lock is free

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$$\Xi \vdash C \parallel o[c, F', \perp_{\text{thread}}] \parallel n\langle t_{\text{blocked}} \rangle \xrightarrow{a}$$

$$\dot{\Xi} \vdash C \parallel C(\Theta') \parallel o[c, F', \mathbf{n}] \parallel n\langle \text{let } x:T = o_r.l(\vec{v}) \text{ in return } x; t_{\text{blocked}} \rangle$$

first attempt

- example: **incoming call** of **synchronized** method
- assume: lock is **free**

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- problem:
 - internal and external behavior **not separated**
 - whether the **incoming call** is possible: *dependent* on the component-**internal** state,² i.e.,
 - the history **trace** doesn't contain enough information to determine enabledness

²Note: for t_{blocked} , the problem is not there even if it looks the same.

“Non-atomic lock grabbing”

- handing over of call:
 - **irrespective** of availability of lock
 - i.e., **no difference** of external/interfaces rules for **synchronized** vs. **non-synchronized** methods!
 - component is **input enabled**

⇒ **lock-grabbing** (of comp. locks) is an **internal step**

- interface interaction: **non-atomic** lock-handling.

“Non-atomic lock grabbing”

- handing over of call:
 - **irrespective** of availability of lock
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⇒ **lock-grabbing** (of comp. locks) is an **internal step**

- interface interaction: **non-atomic** lock-handling.

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Internal steps

$$c[(F, M)] \parallel o[c, F', \perp_{\text{thread}}] \parallel n\langle \text{let } x:T = o.l^s(\vec{v}) \text{ in } t \rangle \xrightarrow{\tau}$$
$$c[(F, M)] \parallel o[c, F', \mathbf{n}] \parallel n\langle \text{let } x:T = M.l^s(o)(\vec{v}) \text{ in } \text{release}(\mathbf{o}); t \rangle$$
$$c[(F, M)] \parallel o[c, F', \mathbf{n}] \parallel n\langle \text{let } x:T = o.l^s(\vec{v}) \text{ in } t \rangle \xrightarrow{\tau}$$
$$c[(F, M)] \parallel o[c, F', \mathbf{n}] \parallel n\langle \text{let } x:T = M.l^s(o)(\vec{v}) \text{ in } t \rangle \quad \text{CALL}_{i_2}^s$$

- 2 internal rules for sync. methods
- note: **re-entrancy**, aux. syntax **release**

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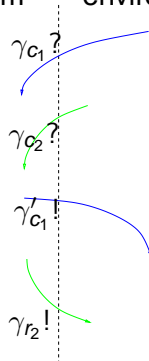
Interface description: Task

- cf. Andreas' talk
- characterize **possible** interface behavior
- possible = adhering to the **restriction** of the language
 - **well-typed**
 - no violation of **mutex**
- rudimentary **trace logic**

Example (1)

- 2 calls, competing for the same (component) **lock**
- **data** dependence
 - σ' received by the first call (of n_1)
 - returned by second thread n_1 afterwards
 - note: σ' is **new**

program environment



- question: *is that trace possible?*

Example (1)

- 2 calls, competing for the same (component) **lock**
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 - o' received by the first call (of n_1)
 - returned by second thread n_1 afterwards
 - note: o' is **new**

$$\gamma_{c_1} ? \gamma_{c_2} ? \gamma'_{c_1} ! \gamma_{r_2} ! \quad =$$

$$(\nu o':c) n_1 \langle \text{call } o.l(o') \rangle ? n_2 \langle \text{call } o.l() \rangle ? n_1 \langle \text{call } \tilde{o}.l() \rangle ! n_2 \langle \text{return}(o') \rangle !$$

- question: *is that trace possible?*

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- question: *is that trace possible?*
- the answer is **no!**
- **data:** “ n_1 before n_2 ”
- **monitors:**
 - the outgoing call of n_1 shows that n_1 must have the lock now
 $\Rightarrow n_2$ cannot have it now: \Rightarrow
“ n_2 before n_1 ”

Example (1)

$$\gamma_{c_1}? \gamma_{c_2}? \gamma'_{c_1}! \gamma_{r_2}! \quad = \quad (\nu o':c) n_1 \langle \text{call } o.l(o') \rangle? n_2 \langle \text{call } o.l() \rangle? n_1 \langle \text{call } \tilde{o}.l() \rangle! n_2 \langle \text{return}(o') \rangle!$$

- question: *is that trace possible?*

$$\gamma_{c_1}? \quad \gamma_{c_2}?$$

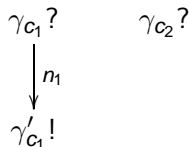
(2)

Note: *non-atomic* lock-grabbing \Rightarrow **no order!**

Example (1)

$$\gamma_{c_1}? \gamma_{c_2}? \gamma'_{c_1}! \gamma_{r_2}! \quad = \quad (\nu o':c) n_1 \langle \text{call } o.l(o') \rangle? n_2 \langle \text{call } o.l() \rangle? n_1 \langle \text{call } \tilde{o}.l() \rangle! n_2 \langle \text{return}(o') \rangle!$$

- question: *is that trace possible?*



(3)

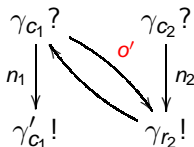
Note: *there is **no** order between events of n_1 and n_2 !*

Example (1)

$$\gamma_{c_1}? \gamma_{c_2}? \gamma'_{c_1}! \gamma_{r_2}! \quad =$$

$$(\nu o':c)n_1\langle \text{call } o.l(o') \rangle? n_2\langle \text{call } o.l() \rangle? n_1\langle \text{call } \tilde{o}.l() \rangle! n_2\langle \text{return}(o') \rangle!$$

- question: *is that trace possible?*



(4)

Note:

- data dependence because of o'

Conditions characterizing monitors

- apart from conditions concerning non-monitor features
 - well-typedness
 - freshness
 - (connectivity)
- 3 types of **dependencies/precedences** between **events**

1. **mutual exclusion:**

*If a thread has taken the lock of a monitor, interactions of other threads with that monitor must either occur **before** the lock is taken, or **after** it has been released again.*

2. **data dependencies:**

*no value (unless generated new) can be transmitted **before** it has been received.*

3. **control dependencies:**

within 1 thread, the events are linearly ordered.

Lock ownership

- question:
given interaction of thread n , is the lock of object o available
- first attempt:
“after call $n \langle \text{call } o.l() \rangle ?$, thread n owns the lock of o .”
- alas: not true!
- complication: non-atomic lock-grabbing
- handing-over call \Rightarrow not necessarily obtaining lock

Lock ownership: non-atomic lock grabbing

- *delayed* observation:

“after $n \langle \text{call } o.l() \rangle ?$, thread n *may* own lock of component object o . ”

- and *later*:

after $n \langle \text{call } o.l() \rangle ? \ n \langle \text{call } o'.l() \rangle !$, thread n *must* own lock of o .

- 2 approximations *per thread*:

- *potential* lock-ownership: “*may*”, written: $\diamond_n o$
- *necessary* lock-ownership: “*must*”, written: $\square_n o$

Lock-ownership: May-approximation

- given the trace t projected to one thread
- from the component-perspective³
after s , the thread may own the lock of o :

$$\Xi \vdash s : \Diamond_o$$

³dually for the environment.

Lock-ownership: May-approximation

$$\frac{\vdash s_2 : \textit{balanced} \quad s_2 \neq \epsilon \quad \exists \vdash s_1 : \Diamond o}{\exists \vdash s_1 \ s_2 : \Diamond o} \text{M-}\Diamond$$

$$\frac{\textit{receiver}(s_1 \gamma_c) = o}{\exists \vdash s_1 \ \gamma_c? : \Diamond o} \text{M-I}\Diamond_1$$

$$\frac{\textit{receiver}(s_1 \gamma_c) \neq o \quad \exists \vdash s_1 : \Diamond o}{\exists \vdash s_1 \ \gamma_c? : \Diamond o} \text{M-I}\Diamond_2$$

$$\frac{\exists \vdash s_1 : \Diamond o}{\exists \vdash s_1 \ \gamma_c! : \Diamond o} \text{M-O}\Diamond$$

Lock-ownership: Must-approximation

- similar system as in the may case
- based on the may-system³
- again from the component-perspective
after s , the thread must own the lock of o :

$$\Xi \vdash s : \Box_o$$

³but no mutual recursion

Lock-ownership: Must-approximation

$$\begin{array}{c} \frac{\Xi \vdash t : \Box o}{\Xi \vdash t_{\gamma_c} ? : \Box o} \text{M-I}\Box_1 \\[2ex] \frac{\Xi \vdash t : \Diamond o}{\Xi \vdash t_{\gamma_c} ! : \Box o} \text{M-O}\Box_1 \end{array} \qquad \begin{array}{c} \Xi \vdash t_{\gamma_r} ? \gamma_r' ! : \Diamond o \\[2ex] \frac{\Xi \vdash t : \Box o}{\Xi \vdash t_{\gamma_r} ? : \Box o} \text{M-I}\Box_2 \\[2ex] \frac{\Xi \vdash t : \Box o}{\Xi \vdash t_{\gamma_r} ! : \Box o} \text{M-O}\Box_2 \end{array}$$

Illustration

Example

$$t = \gamma_c? = (\nu \Xi) n \langle \text{call } o_r. l(o) \rangle? .$$

then

$$\Xi \vdash t : \Diamond_{o_r} \quad \text{and} \quad \Xi \vdash t : \neg \Diamond o$$

Note: \Diamond is a *local* interpretation.

Example

$$t = \gamma_c? \gamma_r! = (\nu \Xi) n \langle \text{call } o_r. l() \rangle? n \langle \text{return}() \rangle! .$$

Then:

$$\Xi \vdash \gamma_c? : \Diamond_n o_r \quad \text{but} \quad \Xi \not\vdash \gamma_c? : \Box_n o_r$$

and

$$\Xi \vdash t : \neg \Diamond_{o_r}$$

Mutual exclusion

- here: again for **component** locks
- “*global*” perspective: not just one thread
- *mutex* precedence edges for event a after r wrt. component object o .

$$M_{\Theta}(ra, o)$$

- auxiliary definitions:
 - “**after may**”: $\Diamond(t, o)$
 - “**before must**”: $\Box(t, o)$
- edges: $\vdash a_1 \rightarrow^m a_2$
- distinction for a between
 - **incoming** communication
 - **no** condition for incoming **returns**
 - incoming calls
 - **outgoing** communication: 2 conditions
 - a **before** other threads have taken the lock
 - **after**

Mutual exclusion

$$M_{\Theta}(r\gamma_c?, o) = \Diamond_{\neq n}(r, o) \rightarrow \gamma_c?$$

$$M_{\Theta}(r\gamma_r?, o) = \{\}$$

$$M_{\Theta}(r\gamma!, o) = \gamma! \rightarrow \Box_{\neq n}(r, o), \\ \Diamond_{\neq n}(r, o) \rightarrow \Box_n(r\gamma!, o)$$

data dependence

- judgment

$$\vdash_{\Theta} r : \gamma? \xrightarrow{d} o$$

if $o \in \text{names}(\gamma)$ and $r'\gamma?$ is a prefix of r .

- “ o is potentially **data-dependent** on event/label $\gamma?$ of trace r ”
- note: it's only potential dependence

$$\begin{aligned} D_{\Theta}(r\gamma!) &= \{\vec{\gamma}? \rightarrow \gamma!\} \quad \text{where } \vdash_{\Theta} \vec{\gamma}? \xrightarrow{d} \text{fn}(\gamma!) \cap \Delta(r) \\ D_{\Theta}(r\gamma?) &= \{\} . \end{aligned}$$

For Δ , the definitions are applied dually.

control dependencies

- precedence nr. 3
 - trivial
- ⇒ the events within each trace are **linearly** ordered
- notation

$$\vdash \mathbf{a'} \rightarrow^c \mathbf{a}$$

putting it together: legal traces

- formal system to characterize interface behavior
- *non-branching* :-)
- judgment:

$$\Xi; G \vdash r \triangleright s : \text{trace}$$

- “after r and with **assumption/commitment**-contexts Ξ and G , the trace s is possible”
- context G :
 - **precedence** graphs
 - cleanly separated into G_{Δ} and G_{Θ}
 - 3 reasons for precedence:
 1. \rightarrow^m
 2. \rightarrow^d
 3. \rightarrow^c
- G must remain **acyclic**: $\vdash G \text{ ok}$

putting it together: legal traces

$$\begin{array}{c} \Xi \vdash r \triangleright o_s \xrightarrow{a} o_r \quad \dot{\Xi} = \Xi + a \quad \dot{\Xi} \vdash a : ok \\ \dot{G}_\Theta = G_\Theta \cup G_\Theta(ra, o_r) \quad \dot{G}_\Delta = G_\Delta \cup G_\Delta(ra, o_s) \quad \vdash \dot{G}_\Delta : ok \\ a = \nu(\Xi'). n\langle call\ o_r.l(\vec{v}) \rangle? \quad \dot{\Xi}; \dot{G} \vdash r\ a \triangleright s : trace \\ \hline \Xi; G \vdash r \triangleright a\ s : trace \end{array} \quad \text{L-CALLI}$$

Results

- Soundness of the abstraction
- in particular: soundness of may and must:

Lemma (Soundness of lock ownership)

1. $\Xi \vdash C \xRightarrow{t} \hat{\Xi} \vdash \hat{C}$ and $\Xi \vdash t : \Box_n o$, then thread n has the lock of o in \hat{C} .
2. If $\Xi \vdash C \xRightarrow{t}$ and $\Xi \vdash t : \Diamond_n o$ and there does not exist an $n' \neq n$ s.t. $\Xi \vdash t : \Box_{n'} o$, then $\Xi \vdash C \xRightarrow{t} \hat{\Xi} \vdash \hat{C}$ for some $\hat{\Xi} \vdash \hat{C}$ s.t. the thread n has the lock of o in \hat{C} .

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Future work

- combination with **cross-border** instantiation/**connectivity**³
- thread **coordination**:⁴
 - **wait**
 - **signal**
- “cleaner” characterization:
 - **non-determinism** is theoretically (and practically) unpleasant
 - better: “**real**” strongest post-condition
 - “event-structures”?

³conceptually not too complicated, technically tricky.

⁴no ideas yet

References I

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